

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Darren L. Anand, et al.

**Examiner:** James C. Kerveros

**Serial No:** 10/707,071

**Art Unit:** 2117

**Filed:** November 19, 2003

**Docket:** BUR920030168US1 (17124)

**For:** AUTOMATIC BIT FAIL MAPPING FOR  
EMBEDDED MEMORIES WITH CLOCK  
MULTIPLIERS

**Date:** October 15, 2007

**Confirmation No:** 1070

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

QAR  
11/17/07

**AMENDMENT UNDER 37 C.F.R. §1.116**

Sir:

In response to the Office Action dated August 15, 2007, applicants submit the following amendments and remarks for consideration by the Examiner and entry of record in the above-identified patent application.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.

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Dated: October 15, 2007

  
Steven Fischman